

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	495	(input or receiver) and (packets or cells or (data same units)) and (load same balancing same (scheme or algorithm)) and (processors or computers or engines) and (output or transmitter)	US- PGPUB; USPAT	2005/05/26 14:49	
2	BRS	L2	65	1 and (receive same (interface or port)) and (shared same memory) and (transmit same (interface or port))	US- PGPUB; USPAT	2005/05/26 14:51	